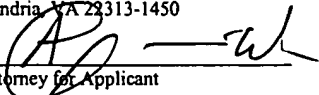


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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

  
Attorney for Applicant

Date: 28 June 2004



Application No. : 10/804,346  
Applicant : Philip Neaves  
Filed : 19 March 2004  
TC/A.U. : Not Yet Assigned  
Examiner : Not Yet Assigned

Atty. Docket No. : DB001095-000  
Customer No. : 24122

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450


**TRANSMITTAL**

Enclosed for filing in the above-captioned application, please find the following:

- An Information Disclosure Statement (3 pgs.);
- Form PTO SB/08A (1 pg.);
- Form PTO SB/08B (1 pg.); and
- copies of 4 cited references.

Also enclosed is a return postcard. Please date stamp and mail the postcard in order to acknowledge receipt of the above-reference correspondence. The Commissioner is hereby authorized to charge any underpayment or credit any overpayment to our Deposit Account No. 20-0888. A duplicate copy of this transmittal is enclosed.

Respectfully submitted,

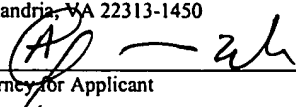
  
Abhijat Parikh  
Reg. No. 49,086  
Thorp Reed & Armstrong, LLP  
One Oxford Centre  
301 Grant Street, 14<sup>th</sup> Floor  
Pittsburgh, Pennsylvania 15219-1425  
Tel.: 412/394-2478  
Fax: 412/394-2555  
Attorney for Applicant

Dated: June 28, 2004



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Customer No. : 24122

To: Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

#### Information Disclosure Statement

Sir:

Pursuant to the provisions of 37 CFR §§1.56 and 1.97-98, enclosed herewith is:

- ☒ Form PTO/SB/08A
- ☒ Form PTO/SB/08B

listing references for consideration by the Examiner.

The filing of this Information Disclosure Statement shall not be construed as a representation regarding the completeness of the list of references, or that inclusion of a reference in this list is an admission that it is "prior art" to this application, or that a search has been made, or as an admission that the information listed is, or may be considered to be, material to patentability as defined in 37 CFR §1.56, or that no other material information exists, and shall not be construed as an admission against applicant(s) interest in any manner.

The Information Disclosure Statement submitted herewith is being filed pursuant to:

☐ 37 C.F.R. §1.97(b)(1); no fee is required;  
OR

☐ 37 C.F.R. §1.97(b)(2); no fee is required;  
OR

☒ 37 C.F.R. §1.97(b)(3); no fee is required;  
OR

☐ 37 C.F.R. §1.97(b)(4); no fee is required;  
OR

☐ 37 C.F.R. §1.97(c);  
AND either

☐ The fee set forth in §1.17(p) is attached; OR

Applicant certifies pursuant to 37 CFR §1.97(e) that:

☐ each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement; OR

☐ no item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing this certification after making reasonable inquiry, no item contained in the Information Disclosure Statement was known to any individual designated under 37 CFR §1.56(c) more than three months prior to the filing of this statement;


OR

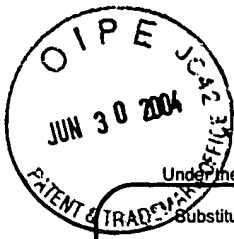
- ☐ 37 C.F.R. §1.97(d); the fee set forth in §1.17(p) is attached,  
AND
- ☐ each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement; OR
- ☐ no item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing this certification after making reasonable inquiry, no item contained in the Information Disclosure Statement was known to any individual designated under 37 CFR §1.56(c) more than three months prior to the filing of this Statement.

Applicant respectfully requests that the listed references be considered by the Examiner, and that the Examiner acknowledge consideration of these references by initialing and returning a copy of the attached form(s) with the next Official Action.

Respectfully submitted,

Dated: 6-28-04

  
Abhijit Parikh  
Reg. No. 49,086  
Thorp Reed & Armstrong, LLP  
One Oxford Centre, 14<sup>th</sup> Floor  
301 Grant Street  
Pittsburgh, Pennsylvania 15219-1425  
Tel.: 412/394-2478  
Fax: 412/394-2555



PTO/SB/08A (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

**Complete if Known**

Application Number	10/804,346
Filing Date	19 March 2004
First Named Inventor	Philip Neaves
Art Unit	Not Yet Assigned
Examiner Name	Not Yet Assigned
Attorney Docket Number	DB001095-000

Sheet 1 of 2

**U. S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
		US- 6,570,437 B2	05-27-2003	Park et al.	
		US- 6,531,857 B2	03-11-2003	Ju	
		US-			
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**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)				

Examiner  
SignatureDate  
Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute for form 1449/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	10/804,346
				Filing Date	19 March 2004
				First Named Inventor	Philip Neaves
				Art Unit	Not Yet Assigned
				Examiner Name	Not Yet Assigned
Sheet	2	of	2	Attorney Docket Number	DB001095-000

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		HIRONORI BANBA et al., A CMOS Bandgap Reference Circuit with Sub-1-V Operation, IEEE Journal of Solid-State Circuits, May 1999, 670-674, Vol. 34, No. 5,	
		K. LASANEN, et al., Design of a 1-V Low Power CMOS Bandgap Reference Based on Resistive Subdivision available at website <a href="http://www.ee.oulu.fi/~Kimi/III-564.pdf">www.ee.oulu.fi/~Kimi/III-564.pdf</a> on 8-/11/03, 4 pgs.	
		F. MALOBERTI, Curvature Compensated Bicmos Bandgap with 1 V Supply Voltage available at website <a href="http://www.ewh.ieee.org/soc/cas/dallas/documents/MalobertiBGAP.pdf">www.ewh.ieee.org/soc/cas/dallas/documents/MalobertiBGAP.pdf</a> on 8/11/03, 29 pgs.	
		M. WALTARI, et al., Reference Voltage Driver for Low-Voltage CMOS A/D Converters available at website <a href="http://www.ecdl.hut.fi/~mwa/publications/2000/ICECS00_1.pdf">www.ecdl.hut.fi/~mwa/publications/2000/ICECS00_1.pdf</a> on 8/11/03, 4 pgs.	

Examiner Signature		Date Considered	
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<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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